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CONFIRMATION NO. 5532

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| SERIAL NUMBER<br>10/552,807 | FILING OR 371(c)<br>DATE<br>10/11/2005<br>RULE | CLASS<br>712 | GROUP ART UNIT<br>2181 | ATTORNEY DOCKET NO.<br>NL 030396 |
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**APPLICANTS**

Bernardo De Oliveira Kastrup Pereira, Eindhoven, NETHERLANDS;

**\*\* CONTINUING DATA \*\*\*\*\***

This application is a 371 of PCT/IB04/50410 04/08/2004

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***

EUROPEAN PATENT OFFICE (EPO) 03101016.8 04/15/2003

**IF REQUIRED, FOREIGN FILING LICENSE GRANTED**

\*\* 06/13/2006

|   |                                 |                        |                    |                         |
|---|---------------------------------|------------------------|--------------------|-------------------------|
| Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no  | STATE OR COUNTRY<br>NETHERLANDS | SHEETS<br>DRAWING<br>5 | TOTAL CLAIMS<br>14 | INDEPENDENT CLAIMS<br>2 |
| 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met<br>Allowance |                                 |                        |                    |                         |
| Verified and Acknowledged<br>Examiner's Signature <u>W.M.T.</u> Initials  |                                 |                        |                    |                         |

**ADDRESS**

24737

**TITLE**

Reconfigurable processor array exploiting ilp and tlp

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